**San Jose State University**

**Department of Computer Engineering**

**CMPE 140 Lab Report**

**Lab 8 Report**

**Title** Pipelined MIPS Processor & I/O Interface

**Semester** \_\_\_\_\_\_\_\_\_\_\_\_ **Date** \_\_\_\_\_\_\_\_\_\_\_\_\_\_

**by**

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*(typed) (typed)*

**Lab Checkup Record**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Week** | **Performed**  **By**  **(signature)** | **Checked**  **By**  **(signature)** | **Tasks**  **Successfully**  **Completed\*** | **Tasks**  **Partially**  **Completed\*** | **Tasks Failed**  **or Not**  **Performed\*** |
| **1** | **A.T.**  **W.M.**  **A.B.** |  |  |  |  |
| **2** |  |  |  |  |  |
| **3** |  |  |  |  |  |
| **4** |  |  |  |  |  |

**\* Detailed descriptions must be given in the report.**